PRAGATI PRITHVI

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ABOUT

Final-year B.Tech student in Electronics and Computer Engineering with hands-on experience in BMS hardware design, PCB schematic and layout using Altium Designer, and EV battery systems. Proven ability to validate hardware through testing and simulation, with exposure to automotive communication protocols and functional safety concepts.

EDUCATION

SRM Institute of Science and Technology (SRMIST) Expected May 2026
Bachelor of Technology – Electronics and Computer Engineering CGPA: 9.5/10
Academic Merit Scholarship (All Semesters) • Best Performer Recognition

SKILLS

PCB Design Tools	Altium Designer, KiCad, EasyEDA, Schematic Capture
Hardware & Electronics	Soldering, Prototyping, Power Electronics
Simulation Tools	MATLAB, Simulink,HIL Testing, Oscilloscope
Embedded Systems	Arduino, ESP32, ESP8266, Arduino Nano, PWM & Motor Control
Programming	C, C++, Python, Embedded C

EXPERIENCE

Model-Based Design Intern – BMS Sep 2025 – Present
Euler Motors *Delhi, India*

- Developed and validated BMS algorithms (temperature anomaly detection, control logic) using MATLAB and Simulink for EV platforms.
- Generated and integrated embedded C code from Simulink models for real-time testing on HIL setups — bridging hardware and software validation.
- Collaborated with cross-functional teams to debug model calibration issues and optimise BMS control logic for efficiency and functional safety.

BMS Hardware & Testing Intern Jun 2025 – Jul 2025
Gajari Brisk (EV Startup) *MNNIT, Prayagraj, India*

- Simulated and configured lithium-ion battery packs optimised for performance, safety, and thermal stability.
- Defined and validated test procedures for SoC and SoH metrics using oscilloscopes and simulation environments.
- Conducted cell balancing logic verification and fault isolation testing — directly relevant to BMS hardware validation.

PROJECTS

Rudra Rover – R25 *PCB Design -Robotics Team, SRMIST*

- Designed and tested electronics boards for power distribution and control — including schematic design, PCB layout, and soldering.
- Ensured subsystem integration across mechanical, software, and hardware teams; validated performance through field testing. [Report]

AgroSense *Dual ESP32 IoT System -Hardware Validation*

- Designed fault-tolerant dual-ESP32 hardware; validated sensor data using a logic analyzer and simulated 5+ fault conditions.
- Recognised with **Honorable Mention** for ML-driven agricultural analysis at 'Unplugged 1.0'. [Report]